

*Article*



# **Analysis, Simulation, and Development of a Low-Cost Fully Active-Electrode Bioimpedance Measurement Module**

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**Abstract:** A low-cost 1 kHz–400 kHz operating frequency fully-active electrode bioimpedance measurement module, based on Howland current source, is presented in this paper. It includes a buffered positive feedback Howland current source, implemented with operational amplifiers, as well as an AD8421 instrumentation amplifier, for the differential voltage measurements. Each active electrode module can be connected to others, assembling a wearable active electrode module array. From this array, 2 electrodes can be selected to be driven from a THS413 fully differential amplifier, activating a mirrored Howland current source. This work performs a complete circuit analysis, verified with MATLAB and SPICE simulations of the current source's transconductance and output impedance over the frequency range between 1 kHz and 1 MHz. Resistors' tolerances, possible mismatches, and the operational amplifiers' non-idealities are considered in both the analysis and simulations. A comparison study between four selected operational amplifiers (ADA4622, OPA2210, AD8034, and AD8672) is additionally performed. The module is also hardware-implemented and tested in the lab for all four operational amplifiers and the transconductance is measured for load resistors of 150  $\Omega$ , 660  $\Omega$ , and 1200  $\Omega$ . Measurements showed that, using the AD8034 operational amplifier, the current source's transconductance remains constant for frequencies up to 400 KHz for a 150  $\Omega$  load and 250 kHz for a 1200  $\Omega$  load, while lower performance is achieved with the other 3 operational amplifiers. Finally, transient simulations and measurements are performed at the AD8421 output for bipolar measurements on the 3 aforementioned load resistor values.

**Keywords:** Howland current source; active electrode; electrical impedance tomography; bioimpedance measurement; instrumentation amplifier; transconductance; output impedance

# **1. Introduction**

Bioimpedance measurements are widely used in many applications to provide quantitative or qualitative information about the human vital signs [\[1–](#page-20-0)[6\]](#page-20-1). Such bio-markers include heart rate (HR), respiration rate (RR), blood pressure (BP), pulse transit time (PPT), and blood glucose levels. These can be acquired either by simple bioimpedance measurement patterns, such as bipolar or tetrapolar measurement, or more complex measurement techniques, such as Electrical Impedance Tomography (EIT) or Electrical Impedance Spectroscopy (EIS) [\[7,](#page-20-2)[8\]](#page-20-3). The latter are also very useful for monitoring time or frequency dependent conditions, such as real-time ventilation, brain hemorrhage, or malignant tumors. Furthermore, bioimpedance measurement techniques are safe, radiation-free, and low-cost [\[7\]](#page-20-2). In most cases, they are also non-invasive, making the measurement process much simpler [\[7\]](#page-20-2).

The essential hardware needed to take bioimpedance measurements usually includes an analog front-end and, in many cases, a digital control for the waveform generation and data acquisition [\[9\]](#page-20-4). The analog front-end is typically composed of a current source,



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the electrodes attached on the subject under test (SUT), a filter, and an instrumentation amplifier [\[10\]](#page-20-5). This common configuration often varies between implemented systems. For instance, there are readout circuit architectures that employ multiple gain and filter stages for the voltage acquisition in order to increase Common-Mode Rejection Ratio (*CMRR*) and decrease the signal noise [\[11–](#page-20-6)[13\]](#page-20-7).

The current sources, often called voltage controlled current sources, (VCCS) can be categorized as voltage-mode and current-mode ones [\[14\]](#page-20-8). The voltage-mode approach is characterized by the current generation through the voltage in one or more of the active circuit's nodes. Such topologies include the well-known Howland current pump (HCP) [\[14–](#page-20-8)[19\]](#page-20-9), the Tietze topology [\[20\]](#page-20-10), the load-in-the-loop topology [\[21\]](#page-20-11), and differential-difference topologies [\[22–](#page-21-0)[24\]](#page-21-1). Current-mode approach makes use of operational transconductance amplifiers (OTAs) [\[7](#page-20-2)[,11](#page-20-6)[,25](#page-21-2)[,26\]](#page-21-3) or current conveyors (CCII) [\[15,](#page-20-12)[27](#page-21-4)[,28\]](#page-21-5) for the current excitation. In all cases, the load driven by the output current can be either grounded or floating.

The implementation of HCPs is usually simple and can be done with commercially available active components (operational amplifiers-Opamps), while the implementation of differential-difference and current-mode VCCS topologies necessitates transistor-level fabrication. Thus, although the latter topologies may offer better performance in terms of the main VCCS evaluation characteristics (large and stable transconductance, large output impedance, large bandwidth, and low Total Harmonic Distortion-*THD*), the availability of discrete active components, makes the HCPs preferable for the implementation of prototype bioimpedance measurement circuits [\[14\]](#page-20-8).

The basic HCP topology was introduced in 1963 [\[22\]](#page-21-0). However, many improvements have been proposed and applied since then. Most of them present topologies based on the Enhanced HCP [\[29\]](#page-21-6), with ground or floating loads and single-ended or mirrored topologies [\[15,](#page-20-12)[18,](#page-20-13)[30,](#page-21-7)[31\]](#page-21-8) (see Figure [1\)](#page-1-0). Further improvements, such as buffered positive feedback [\[32\]](#page-21-9), as in Figure [1,](#page-1-0) lead-lag compensation [\[33](#page-21-10)[–35\]](#page-21-11), HCP followed by load-in-theloop topology stage [\[34\]](#page-21-12), and bridge topologies [\[36\]](#page-21-13).

<span id="page-1-0"></span>

**Figure 1.** Discrete component current source configurations. (**a**) The Enhanced HCP (single-ended). (**b**) The modified buffered HCP used in this work (single-ended). (**c**) Current Conveyor based on the AD844 (Bragos CCII). (**d**) Current Source based on the AD830 differential difference-amplifier (Analog Devices, mirrored).

The HCP's bandwidth often suffers from degradation at even medium frequencies due to the components' and cables' parasitic effects. Specifically, despite the fact that most of the proposed topologies offer wide-band current excitation, in practice, their performance is critically limited by the parasitic capacitances of the electrodes' lead wires. Some techniques have been applied to eliminate their effect. These include the usage of triaxial or shielded cables with buffers [\[37,](#page-21-14)[38\]](#page-21-15), the usage of negative impedance circuits (usually implemented with generalized impedance converters-GICs) [\[38](#page-21-15)-40], or the usage of active electrodes, where either the VCCS, the 1st stage of the voltage readout, or both, are implemented very close to the electrode [\[11,](#page-20-6)[38,](#page-21-15)[41–](#page-21-17)[43\]](#page-21-18). The first solution is expensive, especially for multi-channel systems, and does not compensate for the buffers' input capacitances [\[38\]](#page-21-15). The second one cancels the VCCS' output capacitances; however, it might lead to instability due to overcompensation [\[38\]](#page-21-15). The active electrode has the advantage of avoiding the cables' and multiplexers' (in multi-channel systems) stray capacitances. The use of active electrodes apart from the VCCS' actual bandwidth, also improves the performance of the voltage readout part, since possible channel impedance imbalances before the 1st readout stage are critically decreased [\[42\]](#page-21-19). However, this technique is expensive, since it requires numerous power-consuming active components placed in a small area near each electrode [\[38\]](#page-21-15).

The mentioned active electrode's drawbacks still make their design process somewhat challenging, despite their potential advantages. Actually, only partially-active electrode configurations have been implemented for multi-channel bioimpedance measurement systems (such as EIT) that exclusively use commercially available active components [\[42,](#page-21-19)[43\]](#page-21-18). The partially-active electrode systems implement only the voltage readout stage near each electrode, while the VCCS is implemented on the system's core. This leads to the development of stray capacitances on the VCCS output, due to the cables and the multiplexer switches, drastically reducing the actual advantages of the active electrodes. Fully-active electrodes, which include both the VCCS and the 1st voltage readout stage, have actually been designed and implemented only at integrated chip level [\[11,](#page-20-6)[41,](#page-21-17)[44\]](#page-21-20). However, they require transistor level application-specific integrated circuit (ASIC) fabrication, making them costly and their applicability still limited. For the passive, partially-active, and fully-active electrode measurement configuration schematics, see Figure [2.](#page-2-0)

<span id="page-2-0"></span>

**Figure 2.** Simplified schematics of three bioimpedance measurement configurations. (**a**) Passive electrode configuration. (**b**) Partially-active electrode configuration. (**c**) Fully-active electrode configuration (this work). The cables'/switches' parasitic capacitances are indicated with red color.

In this paper, we attempt to design and implement a low-cost, wearable fully-active electrode bioimpedance measurement module with commercially available components. Each module includes an improved HCP for the generation of the current excitation, a passive electrode connector, an instrumentation amplifier for signal readout and all appropriate signal and power connections between the module and the whole system. At each time, a fully-differential Opamp (which is not included on the modules) generates a differential voltage signal which is provided as an input to the VCCS of two selected modules. In this way, the two selected HCPs act as mirrored ones, while differential voltages from two selected channels are measured from the instrumentation amplifier. We note that voltage measurements can either be bipolar or tetrapolar [\[45,](#page-21-21)[46\]](#page-21-22). The mirrored HCP is numerically analyzed, while both HCPs and the voltage readout were simulated in LT SPICE. Furthermore, the proposed system's modules were implemented in printed circuit boards (PCBs). Measurements over 4 different Opamps' HCPs configurations showed stable transconductance (less than 2% reduction) up to 400 kHz for a 150  $\Omega$  load and up to 250 kHz for a 1200  $\Omega$  load. Moreover, transient measurements at the instrumentation amplifier's output showed low impedance measurement error (0.11–2.36%) at low frequencies, while special account has to be taken for the instrumentation amplifiers' gain degradation over frequency.

Since it acts as an active electrode, this module is wearable, eliminating stray capacitance effects that significantly degrade the VCCS output impedance. It also reduces channel impedance imbalance that causes common signals at the instrumentation amplifier part. Furthermore, it can be applied for EIT/EIS, allowing current pattern selection and/or multifrequency current injections for frequency-difference EIT.

The rest of this paper is organized as follows. Section [2](#page-3-0) presents the proposed activeelectrode bioimpedance measurement module and its function, along with the whole system circuitry. In Section [3,](#page-5-0) an extensive numerical analysis is performed to estimate the transconductance and the output impedance (magnitudes) of the mirrored HCPs, taking into account some Opamps' non-idealities, their behavior over frequency between 1 kHz and 1 MHz, and resistors' mismatches. In Section [4,](#page-11-0) an extensive LT SPICE simulation is performed to estimate the HCP's and the voltage readout performance. Furthemore, in Section [5,](#page-14-0) the measurement results and comparisons over 4 Opamp topologies are presented. Finally, Section [6](#page-19-0) concludes this work.

## <span id="page-3-0"></span>**2. The Proposed Module**

This work proposes a fully active electrode module, as well as a proper configuration and connectivity for bioimpedance measurement techniques, such as EIT and EIS. Briefly, each individual active electrode module consists of a Howland current pump (HCP) voltage-to-current converter source (VCCS) circuit, which is directly connected to the electrode, as shown in Figure [3,](#page-4-0) and an instrumentation amplifier stage.

The implemented VCCS topology is based on the improved HCP [\[15,](#page-20-12)[18\]](#page-20-13). The improved HCP topology is further modified by adding a buffer (B) on the positive feedback path [\[29,](#page-21-6)[32,](#page-21-9)[47\]](#page-21-23). Ideally, a very large output impedance can be achieved by balancing the resistors  $R_1 - R_4$  (Figure [3\)](#page-4-0). However, the actual output impedance is decreased due to the resistors' values imprecision, the Opamps' non-idealities, and the parasitic impedances presented between the HCP's output node and the electrode contact. It has been shown in Reference [\[32\]](#page-21-9) that adding (B) decreases the output impedance's dependence on the resistors' tolerances, as well as the bias current, on the positive feedback path. In addition, it increases the output impedance and the total HCP's slew rate. Furthemore, in the active electrode module, the electrode contact is very close to the HCP's output node, which minimizes the parasitic capacitances caused by the cables in passive configurations [\[11](#page-20-6)[,42](#page-21-19)[,43\]](#page-21-18).

<span id="page-4-0"></span>

**Figure 3.** Brief schematic of the proposed active electrode bioimpedance module and its connectivity.

Each electrode is also connected with an AD8421 instrumentation amplifier through a DC-cutoff 1st-order high pass filter  $(C_1 - R_6)$ . The AD8421 has a sufficiently high input impedance (up to 30 G $\Omega$  || 3 pF for the differential signal) and *CMRR*, which drops from 120 dB at 1 kHz to 85 dB at 100 kHz, for a selected gain of *G* = 10 V/V and no-source imbalance [\[37](#page-21-14)[,48\]](#page-21-24). With a 1 k $\Omega$  source imbalance between the two input channels, the corresponding *CMRR* drops from 85 dB to 47 dB at the corresponding frequencies for this gain. However, since the instrumentation amplifier is embedded on the active electrode module, these imbalances are minimized, preventing the AD8421 *CMRR* from dropping. Furthermore, the 1st order DC-cutoff filter's bias current return resistor  $R_6$  value can be selected at a range of about 10 times the VCCS' output impedance at low frequencies (>5 M $\Omega$ ), in order to prevent degradation. Hence, it will not actually affect the VCCS' bandwidth. Despite the fact that this actually introduces a maximum output impedance limit on the electrode node, it can be proved beneficial for multi-channel measurements at frequencies more than 10 kHz, compared to the passive-electrode configuration. This is because, in the latter case, a multiplexer is placed between the electrode and the instrumentation amplifier, introducing a parasitic capacitance to ground. Its effect, in addition to the cable's parasitics is much more significant beyond this frequency. The *C*<sup>1</sup> capacitor's value must be accordingly adapted, so the filter's cutoff frequency is kept far from the application's frequency range. The instrumentation amplifier's output can be connected to a voltage measurement analog front-end for further processing before the data acquisition [\[13\]](#page-20-7).

The utilization of an Opamp buffer, instead of the AD8421 instrumentation amplifier, could also be considered as the 1st stage for active electrode signal readout. However, a recent study [\[41\]](#page-21-17) has demonstrated that this approach leads to noticeable higher noise levels compared to an active stage providing amplification, such as an instrumentation amplifier topology, especially when potentials are measured from neighboring electrodes.

Each active electrode module is connected to the previous and the next one via a short (3 cm) FPC cable. This allows the corresponding AD8421 to take differential potential measurements from both the electrode on the module it is embedded on and the electrode on the previous active-electrode module (see Figure [3\)](#page-4-0). Furthermore, a THS413 fullydifferential voltage amplifier is used to drive two 180-degrees phase shifted sinusoidal input voltage signals to two selected active electrode modules. This activates the corresponding HCPs to function in mirrored topology mode, which theoretically doubles the VCCS' output impedance [\[18](#page-20-13)[,49\]](#page-21-25). The THS413 is built on an individual PCB board (not on the active electrode modules), which is connected to the active electrode modules via equallength SMA cables. This prevents undesirable phase mismatches between the differential voltage inputs on the HCPs. Each time, the THS413 can drive a pair of active electrodes, which can be selected through low parasitic capacitance and resistance switches (Figure [3\)](#page-4-0). This configuration allows the proposed module's applicability to simple bioimpedance measurements, such as bipolar (current injection and voltage measurement on the same electrode pair) and tetrapolar measurement (current injection and voltage measurement on different electrode pairs), as well as more complex techniques, such as those used in EIT, where an array of multiple electrodes is needed.

## <span id="page-5-0"></span>**3. Circuit Analysis**

In this section, we perform a numerical circuit analysis of the mirrored HCP (the topology of which is shown in Figure [3\)](#page-4-0), using the MATLAB software, to derive its transconductance and output impedance. The resistors' tolerances, which cause undesirable mismatches between the HCP half-circuits, as well as each Opamp's input impedance magnitude  $z_{in}(f)$ , output impedance magnitude  $z_{out}(f)$ , and open-loop gain, are taken into consideration as a function of the sinusoidal input signal's frequency *f* .

To this end, we replace each Opamp with its equivalent circuit in the mirrored-HCP topology, deriving the schematics shown in Figure [4.](#page-5-1) Specifically, Figure [4a](#page-5-1) shows the mirrored-HCP equivalent to estimate the transconductance, while Figure [4b](#page-5-1) shows the configuration to estimate the output impedance. We note that, despite the circuit's symmetry, we perform the analysis in the whole circuitry instead of the half-HCP, in order to analyze the effect of resistors' mismatches.

<span id="page-5-1"></span>

**Figure 4.** The mirrored HCP equivalent circuit for (**a**) the transconductance calculation, (**b**) the *zout* calculation.  $G'_{4,i}$  indicates the parallel combination of  $G_{4,i}$  and  $G_{p,i} = 1/R_{p,i}$ , for  $i = \{1,2\}$ .

For the circuit analysis, we set  $A_1$  and  $B_1$  as the upper half-HCP's Opamps' A and B open-loop gains, respectively (see Figure [3\)](#page-4-0), and *A*<sup>2</sup> and *B*<sup>2</sup> as the down half-HCP's corresponding ones. Furthermore we consider  $Y_{i,S_i} = 1/Z_{in,S_i}$  and  $Y_{o,S_i} = 1/Z_{out,S_i}$  as the corresponding input and output conductivities of the Opamps, for  $S = \{A, B\}$  and  $i = \{1, 2\}$ , as set above. In addition, we assume that

$$
Y_{k,i}(f) = \frac{1}{\sqrt{R_{6,i} + \frac{1}{4\pi^2 C_1^2 f^2}}},\tag{1}
$$

for  $i = \{1, 2\}$ , which is the input conductivity magnitude of the 1st-order high pass filter. For the rest of the resistors, we assume  $G_{j,i} = 1/R_{j,i}$ , for  $j = \{1, 2, 3, 4, 5\}$  and  $i = \{1, 2\}$ . In addition, we set  $Y_L$  as the load's conductivity magnitude.

The typical HCP analysis imposes that the  $R_1 - R_4$  resistor (i.e.,  $G_1 - G_4$  conductivity) values on each half-circuit should match, such as  $R_1 = R_4$  and  $R_2 = R_3$ . However, since the load is floating, the DC voltage values at nodes 6 and 14 (Figure [4a](#page-5-1)) might get very high, often near the Opamps' power supply. This actually could lead the Opamps to saturate and as a consequence, the output current to clip. To avoid this effect in mirrored topologies, Common-Mode feedback configurations have been proposed [\[7,](#page-20-2)[26\]](#page-21-3). Nonetheless, this requires additive active components, which is impractical in the case of a fully-active electrode discrete-element configuration. A possible solution, is to add a large-value resistor  $R_p$  in parallel to  $R_4$  on each half-circuit to set the necessary DC voltage reference for each Opamp A. Despite the fact that this violates the desired balance between the resistors, numerical analysis and simulations show that this affects only the low-frequency output impedance of the HCP. As frequency increases, this violation's effect disappears, without any consequence to the HCP's bandwidth. Furthermore, as already mentioned, the buffer B eliminates the effect of the resistors' imbalances at both the output impedance and the transconductance. However, the usage of resistors of a tolerance of 0.1% or lower is strongly recommended, in order to avoid mismatches between the upper and the down half-HCP, something that could lead to distortion of the output current. Apart from avoiding saturation, fixing the output nodes' DC voltage near ground, maximizes the output voltage swing. This allows larger current amplitudes, always in the safety limits, that lead to increased voltage *SNR*. We note that a high voltage *SNR* is crucial for EIT applications. Finally, by adding a Common Mode Feedback in a future work, the presence of  $R_p$  will not be necessary further.

Taking into consideration the above, the resistors' and capacitor's values were selected as shown in Table [1.](#page-6-0) All the resistor's tolerances were selected at 0.1%, while the capacitors' tolerance is selected at 5%. To estimate the transconductance, numerical computations were performed for 3 different load values:  $150 \Omega$ , 660  $\Omega$ , and  $1200 \Omega$ , that fall within the range of biomedical application interest impedances [\[7](#page-20-2)[,11\]](#page-20-6).

<span id="page-6-0"></span>**Table 1.** Selected nominal values for the resistors and capacitors of the mirrored-HCP.  $i = \{1, 2\}$ .

			$R_{1,i}$ $R_{2,i}$ $R_{3,i}$ $R_{4,i}$ $R_{p,i}$ $R_{5,i}$ $R_{6,i}$ $R_L$ $C_{1,i}$	
			$1 k\Omega$ $1 k\Omega$ $1 k\Omega$ $1 k\Omega$ $100 k\Omega$ $10 k\Omega$ $\{150, 660, 1200\} \Omega$ $10 M\Omega$ $1 nF$	

According to the equivalent, shown in Figure [4a](#page-5-1), the following equations are derived from modified nodal analysis in matrix form

<span id="page-6-1"></span>
$$
K \cdot e = V, \tag{2}
$$

where



$$
Y_{M1} = G_{1,1} + Y_{i,A_1} + G_{2,1}
$$
  
\n
$$
Y_{M2} = Y_{i,A_1} + G_{3,1} + G'_{4,1}
$$
  
\n
$$
Y_{M3} = Y_{o,A_1} + G_{2,1} + G_{5,1}
$$
  
\n
$$
Y_{M4} = G_{5,1} + Y_L + Y_{i,B_1} + Y_{k,1}
$$
  
\n
$$
Y_{M5} = G_{3,1} + Y_{i,B_1} + Y_{o,B_1}
$$
  
\n
$$
Y_{M6} = G_{1,2} + G_{2,2} + Y_{i,A_2}
$$
  
\n
$$
Y_{M7} = G'_{4,2} + G_{3,2} + Y_{i,A_2}
$$
  
\n
$$
Y_{M8} = Y_{o,A_2} + G_{2,2} + G_{5,2}
$$
  
\n
$$
Y_{M9} = Y_L + G_{5,2} + Y_{i,B_2} + Y_{k,1}
$$
  
\n
$$
Y_{M10} = G_{3,2} + Y_{i,B_2} + Y_{o,B_2}
$$
  
\n
$$
G'_{4,i} = 1/R_{4,i} + 1/R_{p,i},
$$
\n(5)

$$
e = [e_1, e_2, ..., e_{16}]^T
$$
 (6)

is the nodes' potentials vector as numbered in Figure [4a](#page-5-1), and

$$
V = [\mathbf{0}_{1 \times 14}, u_{id}, -u_{id}]^T
$$
 (7)

is the vector which denotes the input signals.

The HCP's transconductance is derived from the following expression

<span id="page-7-1"></span>
$$
G_m = \frac{e_6 - e_{14}}{Y_L \cdot u_{id}}.\t\t(8)
$$

From modified nodal analysis at the equivalent circuit, shown in Figure [4b](#page-5-1), the following equation system is derived:

<span id="page-7-0"></span>
$$
L \cdot u = I,\tag{9}
$$

where



$$
Y_{N1} = Y_{i,A_1} + G_{1,1} + G_{2,1}
$$
  
\n
$$
Y_{N2} = G'_{4,1} + Y_{i,A_1} + G_{3,1}
$$
  
\n
$$
Y_{N3} = G_{2,1} + Y_{o,A_1} + G_{5,1}
$$
  
\n
$$
Y_{N4} = Y_{i,B_1} + G_{5,1}
$$
  
\n
$$
Y_{N5} = Y_{i,B_1} + G_{3,1} + Y_{o,B_1}
$$
  
\n
$$
Y_{N6} = Y_{i,A_2} + G_{1,2} + G_{2,2}
$$
  
\n
$$
Y_{N7} = G'_{4,2} + Y_{i,A_2} + G_{3,2}
$$
  
\n
$$
Y_{N8} = G_{2,2} + Y_{o,A_2} + G_{5,2}
$$
  
\n
$$
Y_{N9} = Y_{o,B_2} + G_{5,2}
$$
  
\n
$$
Y_{N10} = Y_{i,B_2} + G_{3,2} + Y_{o,B_2}
$$
  
\n
$$
u = [u_1, u_2, ..., u_{14}]^T
$$
  
\n(12)

is the nodes' potentials vector as numbered in Figure [4b](#page-5-1), and

$$
\mathbf{I} = [0, 0, 0, i_x, 0, 0, 0, 0, i_x, \mathbf{0}_{1 \times 5}]^T
$$
\n(13)

is the vector which denotes the input signals.

The HCP's output impedance is derived from the following expression:

$$
Z_{out} = \frac{e_5 - e_{12}}{i_x} \parallel (1/Y_{k,1} + 1/Y_{k,2}). \tag{14}
$$

Four high-performance Opamps were considered for the numerical analysis: ADA4622, AD8034, AD8672 (Analog Devices), and OPA2210 (Texas Instruments). These Opamps are characterized by very high open-loop gain (at low frequencies), very high input impedance, and low output impedance. They also have very low input bias current and high bandwidth and slew rates of  $18 - 23 \text{ V}/\mu s$ ,  $80 \text{ V}/\mu s$ ,  $6.4 \text{ V}/\mu s$ , and  $4 \text{ V}/\mu s$ , respectively, according to the corresponding manufacturers. All these characteristics are very desirable when selecting an Opamp for HCP topologies [\[18\]](#page-20-13).

For the numerical analysis we assume  $A_1 = B_1 = A_2 = B_2$ ,  $Y_{i,A_1} = Y_{i,A_2} = Y_{i,B_1} = \emptyset$  $Y_{i,B_2}$ , and  $Y_{o,A_1} = Y_{o,A_2} = Y_{o,B_1} = Y_{o,B_2}$ . However, for the resistors and capacitors, we consider the mismatches, such as  $R_{j,1} \neq R_{j,2}$ , for  $j = \{1, 2, ..., 6\}$  and  $C_{1,1} \neq C_{1,2}$ . The analysis is performed over the frequency domain, at the range of 1kHz-1MHz, with 10 steps per decade. A number of  $N = 1000$  runs were performed to numerically solve  $(2)$  and [\(9\)](#page-7-0). At each run, the components' values were randomized, using the mean values shown in Table [1](#page-6-0) and according to the following MATLAB command

$$
R = randn(NoofRuns, 1) * tolerance * Rnom + Rnom \tag{15}
$$

(accordingly for the capacitors). Furthermore, numerical approximations of the Opamps' open-loop gains, input and output impedances were derived for each frequency according to the values given from the corresponding manufacturers (shown in Figure [5\)](#page-9-0). We note that, in this numerical analysis, only the magnitude of each metric is considered.

<span id="page-9-0"></span>

**Figure 5.** Numerical approximations of the open loop gain (**left**), differential input impedance (**center**), and output impedance (**right**) of the ADA4622, AD8034, OPA2210, and AD8672 Opamps (based on Analog Devices and Texas Instruments).

The estimated transconductances of the mirrored-HCP per each Opamp, frequency, and load are demonstrated in Figures [6](#page-9-1)[–9.](#page-10-0) In addition, the estimated output impedances for each Opamp and frequency are presented in Figure [10.](#page-11-1) As observed, the best expected performance is achieved with the AD8034 Opamp, where the expected transconductance is reduced by 1% of its maximum value (10<sup>-4</sup> S) at 400 kHz, 360 kHz, and 320 kHz approximately (for the 150  $\Omega$ , 660  $\Omega$ , and 1200  $\Omega$  load values, respectively). Furthermore, for the AD8034 configuration, the maximum output impedance varies between 2.3 M $\Omega$ and 5.16 M $\Omega$  (0.1% resistor tolerance) at  $1 - 10$  kHz and is expected to drop to approximately 1.35 M $\Omega$  at 100 kHz and 250 k $\Omega$  at 1 MHz. We note that this particular Opamp is characterized by the highest open-loop gain and a relatively high input impedance over the frequency domain (Figure [5\)](#page-9-0). The HCP's transconductance when using the other 3 Opamps fairly drops behind the region of 100 KHz. Furthermore, despite the fact that the maximum output impedance ranges between the same values as in the AD8034 case, it begins to drop from much lower frequencies. Moreover, the load increase from 150  $\Omega$  to 1200  $\Omega$  leads to a slight reduction of the transconductances values above 100 kHz for all Opamps cases.

<span id="page-9-1"></span>

**Figure 6.** Calculated transconductance values of the mirrored-HCP for the ADA4622 Opamp and three resistive load values. The blue line indicates the transconductance when all passive components have their nominal values, while the grey region indicates the effect of their tolerances (0.1% for the resistors, 5% for the capacitors).



**Figure 7.** Calculated transconductance values of the mirrored-HCP for the AD8034 Opamp and three resistive load values. The blue line indicates the transconductance when all passive components have their nominal values, while the grey region indicates the effect of their tolerances (0.1% for the resistors, 5% for the capacitors).



**Figure 8.** Calculated transconductance values of the mirrored-HCP for the OPA2210 Opamp and three resistive load values. The blue line indicates the transconductance when all passive components have their nominal values, while the grey region indicates the effect of their tolerances (0.1% for the resistors, 5% for the capacitors).

<span id="page-10-0"></span>

**Figure 9.** Calculated transconductance values of the mirrored-HCP for the AD8672 Opamp and three resistive load values. The blue line indicates the transconductance when all passive components have their nominal values, while the grey region indicates the effect of their tolerances (0.1% for the resistors, 5% for the capacitors).

<span id="page-11-1"></span>

**Figure 10.** Calculated output impedance values of the mirrored-HCP for the ADA4622, AD8034, OPA2210, and AD8672 Opamps. The read line indicates the transconductance when all passive components have their nominal values, while the grey region indicates the effect of their tolerances (0.1% for the resistors, 5% for the capacitors).

## <span id="page-11-0"></span>**4. SPICE Simulations**

In addition to the previous numerical HCP circuit's analysis, a number of LT SPICE simulations have been performed on the whole bioimpedance module circuitry. The simulations examine the HCP's transconductance for each of the four Opamps and for  $R_L = 150 \Omega$ ,  $R_L = 660 \Omega$ , and  $R_L = 1200 \Omega$ , respectively, as in the numerical analysis. The output impedance's magnitude and phase is also simulated for each Opamp configuration. Each AC simulation was performed for a number of  $N_c = 100$  cases for resistors' tolerance of 0.1% (Monte-Carlo AC simulation), in the range between 1 kHz and 1 MHz (100 points per decade). We note that, in both the transconductance and output impedance simulations, apart from the mirrored HCP, the 1st-order high-pass filter and the AD8421 instrumentation amplifier were considered.

The transconductance SPICE simulation results for all the test cases are shown in Figures [11](#page-12-0)[–14.](#page-12-1) A comparison between the different test cases demonstrates that the AD8034 Opamp-configuration shows an almost constant transconductance (10−<sup>4</sup> A/V) magnitude over a wide frequency range, up to 1 MHz. In addition, the transconductance phase shows a slight decrease, from  $180^\circ$  to  $176.5^\circ$  at 1 MHz, for a 150  $\Omega$  load, and to  $175^\circ$  at 1 MHz for a 1200  $\Omega$  load (for nominal passive components' values). The other 3 Opamp configurations simulations show a less constant transconductance, as well as slightly more transconductance sensitivity to the load resistance. Despite the fact that the SPICE simulation results seem more optimistic than these obtained from the numerical analysis in the previous section (in terms of the magnitude response), they seem to agree at the point that AD8034 performs better in a wider frequency range.

The output impedance SPICE simulation results in Figure [15](#page-13-0) show that, with all the 4 Opamp configurations, the mirrored HCP's output impedance at 1 KHz achieves a magnitude value of  $3 M\Omega$  and a phase near zero degrees. However, the AD8034 case achieves the widest frequency range; at 100 kHz, it is more than 1  $\text{M}\Omega$ , and, at 1 MHz, it drops to 100 kΩ. In addition, the phase also drops slower to −90◦ than in the other Opamps configurations. The other 3 Opamps configurations show larger *zout* drop rates after 10 kHz, with the OPA2210 showing somewhat better performance than the ADA4622 and AD8672. These results overall are in agreement with the numerical analysis performed and indicate that the mirrored-HCP with AD8034 is expected to properly function over

a wide frequency range without significant current loss due the the output impedance, at loads within the biompedances' measurement range.

<span id="page-12-0"></span>

**Figure 11.** Simulated transconductance magnitude and phase values of the mirrored-HCP for the ADA4622 Opamp and three resistive load values at LT SPICE. The light-colored regions indicate the effect of the passive components' tolerances (0.1% for the resistors, 5% for the capacitors).



**Figure 12.** Simulated transconductance magnitude and phase values of the mirrored-HCP for the AD8034 Opamp and three resistive load values at LT SPICE. The light-colored regions indicate the effect of the passive components' tolerances (0.1% for the resistors, 5% for the capacitors).



**Figure 13.** Simulated transconductance magnitude and phase values of the mirrored-HCP for the OPA2210 Opamp and three resistive load values at LT SPICE. The light-colored regions indicate the effect of the passive components' tolerances (0.1% for the resistors, 5% for the capacitors).

<span id="page-12-1"></span>

**Figure 14.** Simulated transconductance magnitude and phase values of the mirrored-HCP for the AD8672 Opamp and three resistive load values at LT SPICE. The light-colored regions indicate the effect of the passive components' tolerances (0.1% for the resistors, 5% for the capacitors).

<span id="page-13-0"></span>

**Figure 15.** Simulated output impedance magnitude and phase values of the mirrored-HCP for the ADA4622, AD8034, OPA2210, and AD8672 Opamps. The light-colored regions indicate the effect of the passive components' tolerances (0.1% for the resistors, 5% for the capacitors).

The MATLAB and SPICE simulation results for the output impedance are quantitatively compared with prior works on HCPs, as well as CCII topologies, in Table [2.](#page-13-1) As mentioned, the selection of the Opamp (for HCP topologies), the stray capacitances, and the resistors' tolerance have significant role in the output impedance value while frequency increases. Hence, apart from the topology used, the Opamp selection and the resistors' tolerance are also mentioned. As observed, the proposed module overall achieves larger output impedance values compared to the cited works, especially at larger frequencies. We note that, except the AD818 case in Reference [\[32\]](#page-21-9), in the other cases, the effect of stray capacitances has either not been considered or not mentioned as to whether it had been considered. At the same time, the stray capacitances in our module are eliminated due to the active electrode design.

Resource	<b>Topology</b>	Opamp	Tolerance	$ Z_{out} $ , 10 kHz	$ Z_{out} $ , 100 kHz	$ Z_{out} $ , 1 MHz
$[15]$	Enhanced HCP	Not Mentioned	$1\%$	750 kQ-4.5 MQ	$670 k\Omega$	$70 k\Omega$
$[15]$	CCII with AD844	No Opamp	$1\%$	$2\,\mathrm{M}\Omega$	288 k $\Omega$ –700 k $\Omega$	$70 \text{ k}\Omega$
$[18]$	Enhanced HCP	<b>OPA655</b>	$1\%$	$80 \text{ k}\Omega - 10 \text{ M}\Omega$	$80 \text{ k}\Omega - 2 \text{ M}\Omega$	$80 \text{ k}\Omega - 300 \text{ k}\Omega$
$[32]$	Enhanced HCP	LM741	$0.1\%$	$200 \text{ k}\Omega$	$10 \text{ k}\Omega$	$5 \,\mathrm{k}\Omega$
$[32]$	<b>Buffered HCP</b>	LM741	$0.1\%$	$300 \text{ k}\Omega$	$20 \text{ k}\Omega$	$7 \text{ k}\Omega$
$[32]$	Bridge HCP	LM741	$0.1\%$	$600 \text{ k}\Omega$	$35 \text{ k}\Omega$	$7 \text{ k}\Omega$
$[32]$	Enhanced HCP (3.5pF C <sub>stray</sub> )	AD818	$0.1\%$	$3\,\mathrm{M}\Omega$	$200 \text{ k}\Omega$	$60 \text{ k}\Omega$
[34]	Enhanced- Optimized <b>Difference</b> Evolution	AD825	0.05%	$5\,\mathrm{M}\Omega$	$3 \text{ M}\Omega$	$100 \text{ k}\Omega$
This Work	Mirrored- Buffered HCP- Active Electrode	AD8034	$0.1\%$	$2.5 \text{ M}\Omega - 4 \text{ M}\Omega$	$0.9 \text{ M}\Omega - 2 \text{ M}\Omega$	$100 \text{ k}\Omega - 250 \text{ k}\Omega$

<span id="page-13-1"></span>**Table 2.** Comparison of implemented discrete-component VCCS output impedances. Ranges denote possible differences between simulated and measured values, and/or variations, due to the resistors' tolerances.

Apart from the AC simulations for the HCP's transconductance and output impedance, a number of transient simulations at the AD8421 instrumentation amplifier's output have

been carried out, specifically for the AD8034 Opamp configuration. The simulations were carried out assuming bipolar measurement, for loads of 150  $\Omega$ , 660  $\Omega$ , and 1200  $\Omega$ , and for sinusoidal input frequencies at 10 kHz and 100 kHz. A number of 100 runs per case (Monte-Carlo simulation) was considered to simulate the passive elements' tolerance effects, while the AD8421's differential gain was fixed to *G* = 5.5 by placing *R<sub>g</sub>* = 2.2 kΩ (see Figure [3\)](#page-4-0). The input voltage signal  $u_{id}$  was set as a sinusoidal signal of a 12.3 $V_{p-p}$  (180<sup>°</sup>) shifted at −*uid*, as shown in Figure [4a](#page-5-1), which is considered as the output of the THS413 fully-differential amplifier. With a HCP transconductance of  $10^{-4}$  S, a current amplitude of  $I_{out} = 1.23$  mA<sub>*p*−*p*</sub> is expected to be produced, resulting at a measured differential voltage amplitude of  $V_o = I_{out} \cdot R_L \cdot G = 6.765 \times 10^{-3} \cdot R_L$  (bipolar measurement).

The transient simulation results are presented in Figure [16](#page-14-1) for input frequency of 10 kHz and in Figure [17](#page-14-2) for input frequency of 100 kHz. The resulting voltage amplitudes are close enough to the expected ones, while the effect of the passive components' low tolerance is not significant. Furthemore, a phase shift is detected (Figures [16c](#page-14-1) and [17c](#page-14-2)), which is expected due to the 1st-order high-pass filters before the AD8421, as well as the HCP itself. Although phase shifts from HCP are undesirable, they usually get particular values for each signal frequency. Thus, since the input signal frequency in bioimpedance measurement applications is known, this phase shift can be predicted and subtracted from the measured signal. In addition, in applications where multiple measurement frames are needed, such as difference-EIT, phase shifts are partially compensated due to the subtraction of the—same frequency—measured signals.

<span id="page-14-1"></span>

**Figure 16.** Transient SPICE analysis on the AD8421 instrumentation amplifier's output for a 12.3*Vp*−*p*, 10 kHz sinusoidal  $u_{id}$ . (**a**) The output voltage for loads of 150 Ω, 660 Ω, and 1200 Ω, in relation to the input. (**b**) The effect of passive components' tolerance to the output's amplitude for  $R_L = 660$  Ω. (**c**) Phase delay between voltage input *uid* and the output.

<span id="page-14-2"></span>

**Figure 17.** Transient SPICE analysis on the AD8421 instrumentation amplifier's output for a 12.3*Vp*−*p*, 100 kHz sinusoidal  $u_{id}$ . (**a**) The ouput voltage for loads of 150  $\Omega$ , 660  $\Omega$ , and 1200  $\Omega$ , in relation to the input. (**b**) The effect of passive components' tolerance to the ouput's amplitude for  $R_L = 660 \Omega$ . (**c**) Phase delay between voltage input *uid* and the output.

#### <span id="page-14-0"></span>**5. Implementation and Measurement Results**

The fully-active electrode bioimpedance measurement module described, analyzed, and simulated was implemented in a 5.94 cm  $\times$  2.74 cm PCB (Figure [18a](#page-15-0)). Each active electrode PCB has a 1 cm diameter hole in its center to adapt an AgCl passive electrode at its back side and can be placed on the Subject Under Test (SUT) to receive bioimpedance

measurements. Apart from the mirrored HCP half-circuit (Figure [18a](#page-15-0) left side), the 1storder high-pass filter and the AD8421 instrumentation amplifier (Figure [18a](#page-15-0) right side), each active electrode PCB includes 6 essential connectors. These connectors are used to receive the input voltage signal from the THS413 fully-differential Opamp, to send the instrumentation amplifiers' output signal to the next front-end stage, and to communicate with the previous and the next active-electrode PCB and to maintain power supply. A THS413 fully-differential Opamp PCB breakout is also implemented to produce the *uid* and −*uid* sinusoidal signals, sending them to two active electrodes via equal length SMA cables.

To get the measurements in this particular work, the sinusoidal waveform in the THS413's input was produced using an Agilent 33220A waveform generator, while the voltages were measured using a Keysight EDUX1002A oscilloscope (see Figure [18b](#page-15-0)). Measurements to estimate the real HCP's transconductance were carried out for all 4 Opamps (ADA4622, AD8034, OPA2210, and AD8672) and load resistor values of 150  $\Omega$ , 660  $\Omega$ , and 1200  $\Omega$ . The transconductance was estimated by measuring the peak-to-peak voltage of each HCP's half-circuit output node, and by using  $(8)$ , for frequencies starting from 1 kHz and ending at the point where the output signal's distortion becomes significant.

<span id="page-15-0"></span>



## (**b**)

**Figure 18.** (**a**) A fully-active electrode bioimpedance module PCB. An AgCl electrode is placed in the large hole in the center. (**b**) The bipolar measurement setup (fully differential amplifier connected with two adjacent active electrode modules and instrumentation).

To reduce possible mismatching effects, as well as to test whether the HCP output nodes have the same voltage amplitude and  $180°$  phase shift, the load  $R_L$  was considered as follows. Two resistors with a value of *RL*/2 were placed in series between the HCP's output nodes, while a ground reference was placed at the *RL*/2 resistors' common node. In bioimpedance applications, a grounded passive electrode can be placed near the current injecting electrodes to cancel parasitic effects between the SUT and the ground (Figure [3\)](#page-4-0). A global Common-Mode feedback circuit could also be considered to eliminate possible mismatching effects; however, this is not included in this particular work. We note that the voltage power supply was set to  $\pm 15$  V for when using the ADA4610, OPA2210, and AD8672 Opamps, and to  $\pm$ 12 V when using the AD8034 Opamp.

A 2.46*Vp*−*<sup>p</sup>* voltage signal was produced from the waveform generator. The THS413 was used to amplify the input signal, producing two output signals of 12.3*Vp*−*p*, shifted by 180 $\degree$ , to drive the mirrored-HCP. Since the HCP's desired transconductance is 10<sup>-4</sup> S, its output nodes must get sinusoidal amplitudes of 92.25*mVp*−*p*, 405.9*mVp*−*p*, and 738*mVp*−*p*, with the ground reference between the *RL*/2 resistors. Furthermore, an exact 180◦ phase shift must occur between the two output nodes. The upper-half HCP output node's voltages for all 3 loads are shown explicitly for the AD8034 implementation, and for 10 kHz, 100 kHz, and 300 kHz input signal frequencies in Figures [19–](#page-16-0)[21.](#page-16-1) It is shown that the voltages measured with the oscilloscope come to agreement with the expected ones that are mentioned above. For lower load resistor values, there is negligible amplitude decrease for frequencies up to 300 kHz. For a 660  $\Omega$  load, we observe no degradation at 100 kHz and a 1% degradation at 300 kHz (398*mVp*−*<sup>p</sup>* from 402*mVp*−*p*). Finally, for a 1200 Ω load, the degradation is more intense (from 736*mVp*−*<sup>p</sup>* at 10 kHz, to 730*mVp*−*<sup>p</sup>* at 100 kHz and 724*mVp*−*<sup>p</sup>* at 300 kHz. In summary, we observe a 1.64% amplitude degradation from 10 kHz to 300 kHz, in this case.

<span id="page-16-0"></span>

**Figure 19.** HCP output node voltages for the AD8034 configuration and 150  $\Omega$  resistive load, when inducing a 10kHz signal (**left**), a 100kHz signal (**center**), and a 300 kHz signal (**right**).



**Figure 20.** HCP output node voltages for the AD8034 configuration and 660 Ω resistive load, when inducing a 10kHz signal (**left**), a 100 kHz signal (**center**), and a 300 kHz signal (**right**).

<span id="page-16-1"></span>

**Figure 21.** HCP output node voltages for the AD8034 configuration and 1200  $\Omega$  resistive load, when inducing a 10 kHz signal (**left**), a 100 kHz signal (**center**), and a 300 kHz signal (**right**).

The HCP's output voltages were measured in a similar way for frequency ranges between 1kHz and the frequency in which the signal is significantly distorted, for all 4 Opamps configurations and the 3 load values. The resulting transconductances, for each case, are shown in Figures [22](#page-17-0)[–25,](#page-17-1) along with the expected ones from the numerical analysis and the SPICE simulations, when passive components have their nominal values. As shown, the ADA4622 and AD8034 configurations achieve wider frequency ranges with almost constant transconductance, while the OPA2210 and AD8672 configurations present significant signal distortion for frequencies beyond 180 kHz (Figures [24](#page-17-2) and [25\)](#page-17-1). Figure [26](#page-18-0) qualitatively shows the sinusoidal signal distortion of the OPA2210 configuration, compared to that of the ADA4622 and the AD8034 configurations at 300 kHz.

<span id="page-17-0"></span>

**Figure 22.** HCP, ADA4622 configuration transconductance results from the numerical analysis, the SPICE simulation (nominal passive component values) and the lab measurements, for  $R_L = 150 \Omega$ ,  $R_L$  = 660  $\Omega$ , and  $R_L$  = 1200  $\Omega$ .



**Figure 23.** HCP, AD8034 configuration transconductance results from the numerical analysis, the SPICE simulation (nominal passive component values) and the lab measurements, for  $R_L = 150 \Omega$ , *R*<sub>*L*</sub> = 660 Ω, and *R*<sub>*L*</sub> = 1200 Ω.

<span id="page-17-2"></span>

**Figure 24.** HCP, OPA2210 configuration transconductance results from the numerical analysis, the SPICE simulation (nominal passive component values) and the lab measurements, for  $R_L = 150 \Omega$ , *R*<sub>*L*</sub> = 660 Ω, and *R*<sub>*L*</sub> = 1200 Ω.

<span id="page-17-1"></span>

**Figure 25.** HCP, AD8672 configuration transconductance results from the numerical analysis, the SPICE simulation (nominal passive component values) and the lab measurements, for  $R_L = 150 \Omega$ , *R*<sub>*L*</sub> = 660 Ω, and *R*<sub>*L*</sub> = 1200 Ω.

<span id="page-18-0"></span>

**Figure 26.** HCP output node voltages for 300 kHz input signal for the ADA4622 configuration (**left**), the AD8034 configuration (**center**), and the OPA2210 (**right**). The resistive load is  $R_L = 660$  Ω. Note the significant distortion for the OPA2210 configuration.

In the ADA4622 case, the transconductance does not degrade within 2% until 500 kHz, 300 kHz, and 250 kHz for 150  $\Omega$ , 660  $\Omega$ , and 1200  $\Omega$  loads, respectively. At the same time, in the AD8034 case, the transconductance degrades more than 2% at 500 kHz (with some small variations), at 400 kHz and 300 kHz for the corresponding load values. Some instrumentation offset errors are also detectable in both cases for 660  $\Omega$  load (2% and 1%, respectively). The measurements results show that both configurations are suitable for the mirrored-HCP, with the AD8034 achieving a minor advantage. As mentioned in Section [3,](#page-5-0) the AD8034 is characterized by the highest Open-loop gain and slew rate, compared to the other 3 Opamps. Furthermore, the measurement results are overall closer to the numerical analysis results than the SPICE simulation results.

In addition to the HCP's transconductance measurement, the voltage output of the AD8421 instrumentation amplifier was tested by performing bipolar measurements for all 3 referred loads. The measurements were performed for a 12.3*Vp*−*<sup>p</sup> uid* input sinusoidal signal (resulting from a 2.46*Vp*−*<sup>p</sup>* signal acted at the THS413 input), at 10 kHz, 100 kHz, and 300 kHz. For the HCP, the AD8034 Opamp was considered. The corresponding output waveforms are shown in Figures [27–](#page-19-1)[29.](#page-19-2) As mentioned in the previous section, the expected output is  $V_0 = 6.765 \times 10^{-3} \cdot R_L$ . Based on this equation and the measurement results, we perform a comparison in Table [3.](#page-18-1)

<span id="page-18-1"></span>**Table 3.** Comparison between expected and measured voltage amplitudes at the AD8421 output (bipolar measurement).



As observed, the output signal's amplitude for certain load values is decreased, while frequency increases. For instance, at 10 kHz we get an impedance measurement error of 0.11–2.36%, while this error becomes significant at 300 kHz (8.17–11.2%). This error, which is larger than the expected one from the transconductance reduction estimated, primarily comes from the instrumentation amplifier's gain degradation when frequency increases. This effect can be significantly reduced with proper calibration, since the measurement frequency is a-priori known in bioimpedance measurement applications. However, a second source of error comes from the instrumentation amplifier's non-linearity error, while additional error is introduced by the common-signal effect and the oscilloscope measurement itself. Such errors' effects are minimized in applications, such as difference-EIT, where two

or more measurement sequences between each electrode pair are acquired. Then, their difference is used as raw data to get a qualitative estimation of the conductivity distribution of the SUT. Finally, it is noted that the bipolar measurements themselves introduce errors related to polarization effects due to the common current injection and voltage measuring path [\[46](#page-21-22)[,50\]](#page-22-0). However, the fully-active electrode bioimpedance measurement modules can be easily applied to tetrapolar measurements.

<span id="page-19-1"></span>

**Figure 27.** AD8421 instrumentation amplifier output for a 150 Ω resistive load and 12.3*Vp*−*<sup>p</sup>* sinusoidal *uid* at 10 kHz (**left**), 100 kHz (**center**), and 300 kHz (**right**). The AD8034 Opamp configuration has been used in the HCP.



**Figure 28.** AD8421 instrumentation amplifier output for a 660  $\Omega$  resistive load and 12.3*V*<sub>*p*−*p*</sub> sinusoidal *uid* at 10 kHz (**left**), 100 kHz (**center**), and 300 kHz (**right**). The AD8034 Opamp configuration has been used in the HCP.

<span id="page-19-2"></span>

**Figure 29.** AD8421 instrumentation amplifier output for a 1200 Ω resistive load and 12.3*Vp*−*<sup>p</sup>* sinusoidal *uid* at 10 kHz (**left**), 100 kHz (**center**), and 300 kHz (**right**). The AD8034 Opamp configuration has been used in the HCP.

#### <span id="page-19-0"></span>**6. Conclusions**

A bioimpedance measurement module, based on a fully-active electrode configuration, was presented in this paper. It consists of a HCP, an electrode connector, a 1st-order highpass filter, and an AD8421 instrumentation amplifier. Multiple bioimpedance measurement modules can be connected, consisting an electrode array for bipolar, tetrapolar, or more complex measuring techiniques, such as EIT and EIS. Two active electrode modules can be selected to inject an AC current, with the corresponding HCPs acting as a mirrored-HCP. Numerical analysis, SPICE simulations, and lab measurements on the implemented setup have shown that the mirrored HCP presents constant transconductance up to 400 kHz when using the AD8034 Opamp. Furthermore, transient bipolar measurements have taken place. As a future work, a global common-mode feedback can be connected to all the modules to further compensate possible mismatches. Furthemore, noise analysis and measurements can further confirm the bioimpedance modules' proper operation [\[51\]](#page-22-1). Finally, an active-electrode array consisting of these modules can be used in numerous applications employing bioimpedance measurements.

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